

## MAGNETORESISTIVE READ HEAD HAVING A BIAS STRUCTURE WITH AT LEAST ONE DUSTING LAYER

### Background of the Invention

#### Field of the Invention

[0001] This invention relates generally to the field of magnetic storage, and more particularly to the field of magnetoresistive read heads.

#### Description of the Related Art

[0002] Magnetoresistive (“MR”) read heads for magnetic storage devices (e.g., disk drives) utilize MR sensors (e.g., giant magnetoresistive or “GMR” sensors) which comprise a ferromagnetic free layer having a magnetization orientation which can be switched between two states by applying a magnetic field. When reading data from magnetic media, magnetic fields from the data bits being read induce the magnetization orientation of the free layer to be in one of the two states. Since the resistance of the MR sensor depends on the magnetization orientation of the free layer, the magnetization state of the free layer can be detected by using a sensing current through the MR sensor to read the data.

[0003] Typically, the MR sensor is formed adjacent to a hard magnetic bias layer which generates a longitudinal magnetostatic bias field for various desirable results. For example, the bias field can provide magnetic stability against domain wall movement within the free layer, thereby reducing noise. In addition, the bias field can enhance the linearity of the MR sensor during the readback operation.

[0004] The bias layer typically comprises a nonmagnetic underlayer and a ferromagnetic hard bias (HB) layer. The underlayer serves to induce desirable morphology (e.g., in-plane crystallographic texture) in the HB layer. Exemplary materials for the underlayer include, but are not limited to, Cr, W, and CrTi alloys. The HB layer produces the longitudinal bias field which is applied to the free layer of the MR sensor. Exemplary materials for the HB layer include, but are not limited to, CoPt, CoCrPt, and other cobalt-based alloys. The remanence moment ( $M_r$ ) and the thickness of the HB layer ( $t$ ) are properly chosen whereby their product ( $M_r \times t$ ) provides a suitable bias field for the MR sensor. In addition, the HB layer preferably has a sufficiently high coercivity ( $H_c$ ) for magnetic stability

of the HB layer, and a preferred in-plane easy axis orientation of magnetization for producing uniform magnetic charge along the edges of the MR sensor to provide the bias field.

**[0005]** The bias field is strongly dependent on the properties of the HB layer (e.g., material, morphology) and on the junction profile between the bias layer and the free layer. Thus, the HB layer significantly impacts such properties of the MR sensor as amplitude, asymmetry, hysteresis, magnetic read width (“MRW”), skirt ratio, and pulse half-width (“PW50”). For example, the granular nature of the HB layer can cause non-uniformity of the bias field at the edges of the free layer. Such non-uniformities can be the source of varying performance properties among manufactured MR sensors, even among MR sensors formed on the same wafer (so-called “nearest neighbor jump,” or “NNJ”), which can vary by 30%-40%. These non-uniformities can be especially influential when dimensions of the MR sensor (e.g., the stripe height) are comparable to the length scale of the grain size of the HB layer.

**[0006]** Generally, a smaller grain size in the HB layer can reduce the non-uniformities due to grain size, as well as reducing the reader noise and further enhancing the signal-to-noise ratio of the MR sensor. However, a reduction of the grain sizes of the HB layer can result in a lower coercivity of the HB layer. It is therefore desirable to reduce the grain sizes of the HB layer while maintaining a high coercivity.

#### Summary of the Invention

**[0007]** Certain embodiments provide a magnetoresistive read head comprising a magnetoresistive sensor and a bias structure adjacent to the magnetoresistive sensor. The bias structure provides a magnetostatic bias field for the magnetoresistive sensor. The bias structure comprises an underlayer, a bias layer over the underlayer, and at least one dusting layer directly below at least one of the underlayer or the bias layer.

**[0008]** Certain embodiments provide a magnetoresistive read head comprising a magnetoresistive sensor and a bias structure adjacent to the magnetoresistive sensor. The bias structure provides a magnetostatic bias field for the magnetoresistive sensor. The bias structure comprises an underlayer and a bias layer over the underlayer. The bias layer further comprises a first dusting layer directly below the underlayer and a second dusting layer directly below the bias layer.

[0009] Certain embodiments provide a method of fabricating a bias structure of a magnetoresistive read head. The magnetoresistive read head comprises a magnetoresistive sensor formed on a substrate. The bias structure is adjacent to the magnetoresistive sensor. The bias structure provides a magnetostatic bias field for the magnetoresistive sensor. The method comprises forming an underlayer and forming a bias layer over the underlayer. The method further comprises forming a dusting layer either directly below the underlayer or directly below the bias layer.

[0010] Certain embodiments provide a magnetoresistive read head comprising a magnetoresistive sensor and a bias structure adjacent to the magnetoresistive sensor. The bias structure provides a magnetostatic bias field for the magnetoresistive sensor. The bias structure comprises an underlayer having an average underlayer grain size and a bias layer over the underlayer. The bias layer has an average bias layer grain size larger than the average underlayer grain size.

Brief Description of the Drawings

[0011] Figures 1A-1C schematically illustrate various embodiments of the present invention with an abutted junction structure.

[0012] Figures 2A-2F schematically illustrate an exemplary method of forming the MR read head with a continuous or abutted junction with a dusting layer directly below the underlayer.

[0013] Figure 3A is a 5μm-by-5μm atomic-force-microscopy (“AFM”) image of a 3-Angstrom-thick tungsten (W) dusting layer deposited on an aluminum oxide coating of a silicon substrate.

[0014] Figure 3B is a 5μm-by-5μm AFM image of a 7-Angstrom-thick W dusting layer on an aluminum oxide coating of a silicon substrate.

[0015] Figure 4A is a 10μm-by-10μm magnetic-force-microscopy (“MFM”) image of a Si/Al<sub>2</sub>O<sub>3</sub>/70ÅCr/195ÅCoPt structure.

[0016] Figure 4B is a 10μm-by-10μm MFM image of a Si/Al<sub>2</sub>O<sub>3</sub>/3ÅW/70ÅCr/195ÅCoPt structure.

[0017] Figure 5A is a 350nm-by-350nm plane-view transmission-electron-microscopy (“TEM”) image of a Si/Al<sub>2</sub>O<sub>3</sub>/70ÅCr/195ÅCoPt structure.

[0018] Figure 5B is a 350nm-by-350nm plane-view TEM image of a Si/Al<sub>2</sub>O<sub>3</sub>/3ÅW/70ÅCr/195ÅCoPt structure.

[0019] Figure 6 is a vibrating sample magnetometer (“VSM”) measurement of the magnetic properties of a Si/Al<sub>2</sub>O<sub>3</sub>/W(t)/70ÅCr/195ÅCoPt structure as a function of the thickness t of the grain size control W dusting layer.

[0020] Figures 7A-7D schematically illustrate an exemplary method of forming a continuous or abutted junction MR read head with a dusting layer directly below the HB layer.

[0021] Figure 8 illustrates a VSM measurement of the magnetic properties of a Si/Al<sub>2</sub>O<sub>3</sub>/3ÅW/70ÅCr/WTi(t)/195ÅCoPt structure as a function of the thickness t of the WTi second dusting layer.

[0022] Figure 9 is a 350nm-by-350nm plane-view TEM image of a Si/Al<sub>2</sub>O<sub>3</sub>/3ÅW/70ÅCr/7ÅWTi/195ÅCoPt structure.

Detailed Description of the Preferred Embodiment

[0023] Figures 1A-1C schematically illustrate various embodiments of the present invention with an abutted junction structure. A magnetoresistive (“MR”) read head 10 comprises a MR sensor 20 and a bias structure 30 adjacent to the MR sensor 20. The bias structure 30 provides a magnetostatic bias field for the MR sensor 20. The bias structure 30 comprises an underlayer 40, a hard bias (“HB”) layer 50 over the underlayer 40, and at least one dusting layer 60. The dusting layer 60 is directly below at least one of the underlayer 40 or the HB layer 50. In the embodiment schematically illustrated by Figure 1A, the dusting layer 60a is directly below the underlayer 40. In the embodiment schematically illustrated by Figure 1B, the dusting layer 60b is directly below the HB layer 50. In embodiments with two dusting layers, such as the embodiment schematically illustrated by Figure 1C, a first dusting layer 60a is directly below the underlayer 40 and a second dusting layer 60b is directly below the HB layer 50. In certain embodiments, such as those illustrated by Figures 1A-1C, the MR read head 10 further comprises an electrically conductive lead layer 70 over the HB layer 50.

[0024] In certain embodiments, the MR sensor 20 is selected from a group consisting of a giant magnetoresistive (GMR) sensor, an anisotropic magnetoresistive (AMR) sensor, a tunneling magnetoresistive (TMR) sensor, a spin-dependent-tunneling (SDT)

sensor, a spin valve (SV) sensor, a current-in-plane (CIP) sensor, and a current-perpendicular-to-the-plane (CPP) sensor. In embodiments in which the MR sensor 20 comprises a patterned GMR stack, as schematically illustrated by Figure 1A, the MR sensor 20 comprises a dielectric layer 22 (e.g., aluminum oxide) formed on a substrate 23 (e.g., silicon), a pinning layer 24 over the dielectric layer 22, a spacer layer 25 over the pinning layer 24, a pinned layer 26 over the spacer layer 25 and antiferromagnetically coupled to the pinning layer 24, a free layer 28 over the pinned layer 26, and an overlayer 29. The GMR-stack structure of the MR sensor 20 of Figure 1A is exemplary; other types and structures of MR sensors 20 are compatible with embodiments described herein.

**[0025]** In certain embodiments, the underlayer 40 comprises a material selected from a group consisting of chromium, chromium-containing alloy, tungsten, tungsten-containing alloy, nickel-aluminum alloy, and iron-aluminum alloy. The chromium-containing alloy can comprise a material selected from a group consisting of titanium, vanadium, molybdenum, maganese, and tungsten. The tungsten-containing alloy can comprise a material selected from a group consisting of chromium, titanium, vanadium, and molybdenum. The material of the underlayer 40 of certain embodiments is selected to provide good lattice match with the material of the HB layer 50 (e.g., Cr underlayer 40 with a CoPt HB layer 50). In certain embodiments, the underlayer 40 has a thickness in a range from approximately 20 Angstroms to approximately 250 Angstroms. In still other embodiments, the underlayer 40 has a thickness in a range from approximately 70 Angstroms to approximately 200 Angstroms. Other materials and thicknesses of the underlayer 40 are compatible with embodiments described herein.

**[0026]** In certain embodiments, the HB layer 50 comprises a material selected from a group consisting of CoPt, CoCrPt, CoCrPtTa, CoCrPtB, CrPt, CoPt, and FePt. In other embodiments, the HB layer 50 comprises a magnetic oxide (e.g., ferrous oxide). The material of the HB layer 50 of certain embodiments is selected to provide a sufficiently high bias field to the MR sensor 20 to improve performance. In certain embodiments, the HB layer 50 has a thickness in a range from approximately 75 Angstroms to approximately 300 Angstroms. In still other embodiments, the HB layer 50 has a thickness in a range from

approximately 100 Angstroms to approximately 250 Angstroms. Other materials and thicknesses of the HB layer 50 are compatible with embodiments described herein.

**[0027]** In certain embodiments, as schematically illustrated by Figure 1A, the MR read head 10 comprises a dusting layer 60a which is directly below the underlayer 40. The dusting layer 60a of certain embodiments comprises discontinuous, densely-packed, small islands of material. In certain such embodiments, the dusting layer 60a comprises a material having a sufficiently high surface energy and a sufficiently low atomic mobility to form the islands.

**[0028]** In certain embodiments, the dusting layer 60a comprises a material having a body-centered-cubic crystallographic structure, while in other embodiments, the dusting layer 60a comprises a material having a CsCl-type crystallographic structure. Exemplary materials for the dusting layer 60a can be selected from the group consisting of tungsten, tantalum, niobium, rhodium, molybdenum, tungsten-titanium alloy, tungsten-chromium alloy, and nickel-aluminum alloy. Still other embodiments utilize a dusting layer 60a comprising platinum or titanium. The dusting layer 60a of certain embodiments comprises a material having a melting temperature above the melting temperature of the underlayer 40. In other embodiments, the dusting layer 60a comprises a material having a melting temperature above 1800 degrees Celsius. Such high melting point materials have relatively high surface energy and low atomic mobility so as to form high density, nano-sized, island-like grains. Other materials for the dusting layer 60a are compatible with embodiments described herein.

**[0029]** In certain embodiments, the dusting layer 60a has a thickness less than approximately 10 Angstroms. In certain other embodiments, the dusting layer 60a has a thickness in a range from approximately 2 Angstroms to approximately 6 Angstroms. In still other embodiments, the dusting layer 60a has a thickness of approximately 3 Angstroms. The dusting layer 60a differs from multilayer underlayer structures, which have components which are significantly thicker (e.g., tens of Angstroms) than the dusting layer 60a.

**[0030]** Figures 2A-2F schematically illustrate an exemplary method of forming a continuous or abutted junction MR read head 10 with a dusting layer 60a, as schematically illustrated by Figure 1A. A MR sensor stack 110 having a dielectric layer 112 and a MR layer 113 is first provided on a substrate 114, as shown in Figure 2A. The MR layer 113 of

certain embodiments comprises multiple layers, including the free layer 28, as shown in Figure 1A. The MR sensor stack 110 is then patterned by etching or milling using a bi-layer (lift-off) photoresist mask. In certain embodiments, an ion-beam milling method is used to fabricate the MR sensor 20. Typically, the ion-beam milling is controlled to stop in the dielectric layer 112 using end-point detection technique, as shown in Figure 2B. Alternatively, the ion-beam milling can be controlled to stop in another layer of the MR sensor stack 110 (e.g., PtMn, NiFe, CoFe). An exemplary method of ion-beam milling is described by U.S. Patent No. 5,664,316 issued to Chen et al., which is incorporated in its entirety by reference herein. By milling and then removing the photomask, the structure shown Figure 2B with a milled surface at the edge of the MR sensor 20 is formed. Persons skilled in the art are able to use other methods of fabricating the MR sensor 20 in accordance with embodiments described herein.

[0031] The dusting layer 60a is then deposited on the milled surface of the MR sensor 20, resulting in the structure shown in Figure 2C. The dusting layer 60a of certain embodiments comprises discontinuous, densely-packed, small islands of material on the milled surface, as shown schematically in Figure 2C. In certain embodiments, ion-beam deposition (“IBD”) is used to deposit the dusting layer 60a onto the milled surface. Because the dusting layer 60a is relatively thin (e.g., approximately 3-6 Angstroms), the deposition rate is kept relatively low. For example, in certain embodiments using a IBD Model No. 350 tool from Veeco Instruments, Inc. of Woodbury, New York, the deposition energy is maintained to be approximately 650 Volts with a beam current of approximately 150 milliamps, thereby yielding a deposition rate of approximately 0.3 Angstroms/second. Other embodiments can utilize other ion-beam-deposition tools (e.g., NEXUS IBD tool from Veeco Instruments, Inc.).

[0032] If the thickness of the dusting layer 60a is above 10 Angstroms, the dusting layer 60a becomes less island-like and more continuous, so the thickness of the dusting layer 60a is preferably carefully monitored. In certain embodiments, the deposition of the dusting layer 60a is performed at a deposition rate of approximately 0.3 Angstroms/second. In certain such embodiments, the deposition is performed for a time period having a duration ranging from approximately 8 seconds to approximately 30 seconds.

In certain other embodiments, the deposition of the dusting layer 60a is performed at a deposition rate of approximately 0.7 Angstroms/second. In certain such embodiments, the deposition is performed for a time period having a duration ranging from approximately 2 seconds to approximately 15 seconds.

**[0033]** The underlayer 40 is then deposited over the dusting layer 60a, resulting in the structure shown in Figure 2D. Continuing the fabrication of the MR read head 10 of Figure 1A, the HB layer 50 is then deposited over the underlayer 40, resulting in the structure shown in Figure 2E. The underlayer 40 establishes the desired c-axis in-plane orientation of the HB layer 50. In certain embodiments, the center of the HB layer 50 is approximately aligned with the free layer of the MR sensor 20. Subsequently, the lead layer 70 is deposited over the HB layer 50, resulting in the structure shown in Figure 2F. The lead layer 70 comprises a conductive material (e.g., Au, W, Rh, Cr, Cu) with a range of thicknesses between about 100 Angstroms and about 1000 Angstroms thick. The lead layer 70 provides the electrical connections to the MR sensor 20 for the sensing current. In certain embodiments, IBD is used to deposit the underlayer 40, the HB layer 50, and the lead layer 70. Persons skilled in the art can select appropriate materials, thicknesses, and deposition parameters for these layers.

**[0034]** The dusting layer 60a influences the morphology (e.g., grain sizes) of the underlayer 40 deposited on the dusting layer 60a. For example, using the dusting layer 60a can result in an underlayer 40 with small grain sizes, e.g., less than approximately 150 Angstroms.

**[0035]** In addition, in embodiments in which the underlayer 40 is sufficiently thin, the dusting layer 60a can also influence the morphology of the HB layer 50 deposited on the underlayer 40. For example, using the dusting layer 60a and a sufficiently thin underlayer 40 (e.g., less than 170 Angstroms), both the underlayer 40 and the HB layer 50 can have grain sizes of approximately 100-110 Angstroms. Thus, in such embodiments with the dusting layer 60a and a thin underlayer 40, the grain sizes of the HB layer 50 are affected by the dusting layer 60a. For this reason, the dusting layer 60a is sometimes referred to as the “grain size control” dusting layer.

[0036] The influence of the dusting layer 60a on the grain sizes of the HB layer 50 is more pronounced for thinner underlayers 40 and for dusting layers 60a which are more island-like (e.g., less than 10 Angstroms in thickness). The island-like grains of the dusting layer 60a are thought to act as nucleation sites for grain growth for the underlayer 40, thereby significantly reducing the grain sizes of the underlayer 40. The smaller grains of the underlayer 40 are thought to then act as nucleation sites for grain growth for the HB layer 50, thereby significantly reducing the grain sizes of the HB layer 50.

[0037] Figure 3A is an atomic-force-microscopy (“AFM”) image of a 3-Angstrom-thick tungsten (W) dusting layer 60a deposited on an aluminum oxide coating of a silicon substrate. Figure 3B is an AFM image of a 7-Angstrom-thick W dusting layer 60a on an aluminum oxide coating of a silicon substrate. Each of Figures 3A and 3B illustrates an approximately 5 microns by 5 microns square area of the W dusting layer 60a. The morphology of the 3-Angstrom-thick W dusting layer 60a includes discrete W islands, as shown in Figure 3A. As the thickness of the W dusting layer 60a is increased to 7 Angstroms, as shown in Figure 3B, more surface coverage of the dusting layer 60a is formed, although it remains discontinuous.

[0038] Figure 4A is a magnetic-force-microscopy (“MFM”) image of a 195-Angstrom-thick CoPt HB layer 50 on a 70-Angstrom-thick Cr underlayer 40 on an Al<sub>2</sub>O<sub>3</sub> dielectric layer 22 on a Si substrate 23 (denoted by Si/Al<sub>2</sub>O<sub>3</sub>/70ÅCr/195ÅCoPt). Figure 4B is an MFM image of a similar structure, but with a 3-Angstrom-thick W dusting layer 60a between the Al<sub>2</sub>O<sub>3</sub> dielectric layer 22 and the Cr underlayer 40 (denoted by Si/Al<sub>2</sub>O<sub>3</sub>/3ÅW/70ÅCr/195ÅCoPt). Each of Figures 4A and 4B illustrates an approximately 10 microns by 10 microns square area of the HB layer 50. Comparison of Figure 4A and Figure 4B illustrates that the HB layer 50 with the 3-Angstrom-thick W dusting layer 60a exhibits smaller magnetic domain sizes, indicating the magnetic grain size reduction in the HB layer 50 resulting from the 3-Angstrom-thick dusting layer 60a.

[0039] Figure 5A is a plane-view transmission-electron-microscopy (“TEM”) image of a 195-Angstrom-thick CoPt HB layer 50 on a 70-Angstrom-thick Cr underlayer 40 on an Al<sub>2</sub>O<sub>3</sub> dielectric layer 22 on a Si substrate 23 (i.e., Si/Al<sub>2</sub>O<sub>3</sub>/70ÅCr/195ÅCoPt). Figure 5B is a plane-view TEM image of a similar structure, but with a 3-Angstrom-thick W dusting

layer 60a between the Al<sub>2</sub>O<sub>3</sub> dielectric layer 22 and the Cr underlayer 40 (i.e., Si/Al<sub>2</sub>O<sub>3</sub>/3ÅW/70ÅCr/195ÅCoPt). Each of Figures 5A and 5B illustrates an approximately 350 nanometers by 350 nanometers square area of the HB layer 50. For a conventional structure without the dusting layer 60a, Figure 5A illustrates a polycrystalline grain structure with an average grain size of approximately 17.6 nanometers. For a structure which includes the dusting layer 60a, Figure 5B illustrates a much smaller grain size of approximately 10 nanometers. In addition to the grain size reduction, the W dusting layer 60a has improved the grain size uniformity, which is also desirable.

**[0040]** The magnetic properties of the bias structure Si/Al<sub>2</sub>O<sub>3</sub>/W(t)/70ÅCr/195ÅCoPt as a function of the thickness t of the grain size control W dusting layer 60a were measured using a vibrating sample magnetometer (“VSM”), and the results are illustrated in Figure 6. The coercivity H<sub>c</sub> initially decreases with increasing thickness of the W dusting layer 60a, and reaches a minimum value at a thickness of approximately 3 Angstroms. Further increases of the thickness of the W dusting layer 60a to a thickness of approximately 10 Angstroms result in a rebound of H<sub>c</sub>, reaching the same value as in a film without the W dusting layer 60a. While the dusting layer 60a reduces the grain sizes of the HB layer 50, it also reduces the coercivity of the HB layer 50.

**[0041]** The squareness S, defined as S=M<sub>r</sub>/M<sub>s</sub> (where M<sub>r</sub> is the remanence and M<sub>s</sub> is the saturation magnetization), and the coercivity squareness S\*, defined as S\*=1-(M<sub>r</sub>/H<sub>c</sub>)(1/slope at H<sub>c</sub>), show similar dependencies on the thickness of the dusting layer 60a. TEM studies indicate that the increase of H<sub>c</sub> at thicker W dusting layers 60a is accompanied by an increase in the grain size of the CoPt HB layer 50, possibly due to the diminishing effect of the grain size control dusting layer 60a. Other embodiments using dual dusting layers 60a, 60b can be used to obtain high H<sub>c</sub> while maintaining the small grain size achieved by using the grain size control dusting layer 60a, as described in more detail below.

**[0042]** Figure 1B schematically illustrates another embodiment in which the MR read head 10 comprises a dusting layer 60b which is directly below the HB layer 50. As described above with regard to the dusting layer 60a, the dusting layer 60b of certain embodiments comprises discontinuous, densely-packed, small islands of material. In certain such embodiments, the dusting layer 60b comprises a material having a sufficiently high

surface energy and a sufficiently low atomic mobility to form the islands. In other embodiments, the dusting layer 60b is a continuous layer with good lattice match to both the underlayer 40 and the HB layer 50.

[0043] Similarly to the dusting layer 60a, in certain embodiments, the dusting layer 60b comprises a material having a body-centered-cubic crystallographic structure, while in other embodiments, the dusting layer 60b comprises a material having a CsCl-type crystallographic structure. Exemplary materials for the dusting layer 60b can be selected from the group consisting of tungsten, tantalum, niobium, rhodium, molybdenum, tungsten-containing alloy, chromium-containing alloy, tungsten-titanium alloy, tungsten-chromium alloy, and nickel-aluminum alloy. The dusting layer 60b of certain embodiments comprises a material having a melting temperature above the melting temperature of the underlayer 40. In other embodiments, the dusting layer 60b comprises a material having a melting temperature above 1800 degrees Celsius. Such high melting point materials have relatively high surface energy and low atomic mobility so as to form high density, nano-sized, island-like grains. Other materials for the dusting layer 60b are compatible with embodiments described herein.

[0044] In certain embodiments, the dusting layer 60b has a thickness less than approximately 10 Angstroms. In certain other embodiments, the dusting layer 60b has a thickness in a range from approximately 5 Angstroms to approximately 10 Angstroms. In still other embodiments, the dusting layer 60b has a thickness of approximately 7 Angstroms. The dusting layer 60b differs from multilayer underlayer structures, which have components which are significantly thicker (e.g., tens of Angstroms) than the dusting layer 60b.

[0045] Figures 7A-7D schematically illustrate an exemplary method of forming a continuous or abutted junction MR read head 10 with the dusting layer 60b, as schematically illustrated by Figure 1B. Figure 7A schematically illustrates a patterned MR sensor 20 which is the same as that shown in Figure 2B, and can be formed using the same techniques as described above in relation to Figure 2B. Persons skilled in the art are able to use other methods of fabricating the MR sensor 20 in accordance with embodiments described herein.

[0046] The underlayer 40 is deposited on the milled surface of the MR sensor 20, resulting in the structure shown in Figure 7B. The dusting layer 60b is then deposited on the underlayer 40, resulting in the structure shown in Figure 7C. The dusting layer 60b of certain

embodiments comprises discontinuous, densely-packed, small islands of material on the underlayer 40, as shown schematically in Figure 7C. In other embodiments, the dusting layer 60b is a continuous layer with good lattice match to both the underlayer 40 and the HB layer 50. As described above in relation to the dusting layer 60a, the dusting layer 60b of certain embodiments is deposited on the underlayer 40 using IBD techniques. If the thickness of the dusting layer 60b is above 10 Angstroms, the dusting layer 60b becomes less island-like and more continuous, so the thickness of the dusting layer 60b is preferably carefully monitored.

[0047] In certain embodiments, the deposition of the dusting layer 60b is performed at a deposition rate of approximately 0.3 Angstroms/second. In certain such embodiments, the deposition is performed for a time period having a duration ranging from approximately 15 seconds to approximately 30 seconds. In certain other embodiments, the deposition of the dusting layer 60b is performed at a deposition rate of approximately 0.7 Angstroms/second. In certain such embodiments, the deposition is performed for a time period having a duration ranging from approximately 7 seconds to approximately 15 seconds.

[0048] Continuing the fabrication of the MR read head 10 of Figure 1B, the HB layer 50 is then deposited over the dusting layer 60b. In certain embodiments, the center of the HB layer 50 is approximately aligned with the free layer of the MR sensor 20. Subsequently, the lead layer 70 is deposited over the HB layer 50, resulting in the structure shown in Figure 7D. As described above in relation to Figures 2D-2F, in certain embodiments, IBD is used to deposit the underlayer 40, the HB layer 50, and the lead layer 70. Persons skilled in the art can select appropriate materials, thicknesses, and deposition parameters for these layers.

[0049] The dusting layer 60b influences the morphology (e.g., texture) of the HB layer 50 deposited on the dusting layer 60b. For this reason, the dusting layer 60b is sometimes referred to as the “texture control” dusting layer. The texture control dusting layer 60b modifies the crystallography of the HB layer 50 by inducing better in-plane texture and thus higher coercivity due to better lattice match between the dusting layer 60b and the HB layer 50. Due to the small thickness of the dusting layer 60b, the higher coercivity is achieved without significant penalty of increasing grain size in the HB layer 50. In embodiments in which the dusting layer 60b has an island-like structure, the islands act as

nucleation sites for grain growth for the HB layer 50. In embodiments in which the dusting layer 60b is continuous, the lattice of the dusting layer 60b as a template for growth of the HB layer 50.

**[0050]** As described above, embodiments utilizing the grain size control dusting layer 60a alone produce small grain sizes in the HB layer 50, and a corresponding reduction of the coercivity  $H_c$ . However, embodiments utilizing the texture control dusting layer 60b directly below the HB layer 50 in conjunction with the grain size control dusting layer 60a directly below the underlayer 40 can maintain relatively small grain sizes while achieving higher coercivities  $H_c$ . One embodiment of such a bias structure 40 is shown schematically in Figure 1C.

**[0051]** Such embodiments can be fabricated by an exemplary method which combines the two processes outlined by Figures 2A-2F and Figures 7A-7D. After patterning the MR sensor 20 by ion-beam milling, the first dusting layer 60a is deposited on the milled surface. The underlayer 40 is then deposited on the first dusting layer 60a, and the second dusting layer 60b is deposited on the underlayer 40. The HB layer 50 is then deposited on the second dusting layer 60b, and the lead layer 70 is deposited on the HB layer 50. In certain embodiments, the first dusting layer 60a and the second dusting layer 60b comprise the same material, while in other embodiments, the two dusting layers 60a and 60b comprise different materials. Persons skilled in the art can select appropriate materials, thicknesses, and deposition parameters for the various layers.

**[0052]** In embodiments using only the grain size control dusting layer 60a, the grain sizes of the HB layer 50 and of the underlayer 40 are substantially the same (e.g., 100-110 Angstroms). However, in embodiments which also use the texture control dusting layer 60b, the grain sizes of the HB layer 50 are increased (e.g., 140-150 Angstroms) to be larger than the grain sizes of the underlayer 40 (e.g., 100-110 Angstroms). Thus, in certain embodiments, the bias layer has an average grain size which is larger than an average grain size of the underlayer 40.

**[0053]** Figure 8 illustrates the VSM-measured magnetic properties of such a bias structure 30 with the two dusting layers 60a, 60b. The bias structure 30 comprises a 3-Angstrom-thick W first dusting layer 60a deposited on  $\text{Al}_2\text{O}_3$ , a 70-Angstrom-thick Cr

underlayer 40 deposited on the first dusting layer 60a, a WTi second dusting layer 60b deposited on the underlayer 40, and a 195-Angstrom-thick CoPt HB layer 50 deposited on the second dusting layer 60b (i.e., Si/Al<sub>2</sub>O<sub>3</sub>/3ÅW/70ÅCr/WTi(t)/195ÅCoPt) as a function of the thickness t of the WTi second dusting layer 60b. As shown by Figure 8, by using the second WTi dusting layer 60b, H<sub>c</sub> is improved from 1100 Oersteds to about 2400 Oersteds with a WTi thickness of approximately 5 Angstroms. Such improvement is attributable to better lattice matching between the WTi (with an atomic percentage of Ti of approximately 20%) and the CoPt (with an atomic percentage of Pt of approximately 20%).

[0054] Figure 9 illustrates a 350-nanometer-by-350-nanometer plane-view TEM image of the grains of a bias structure 30 with a 3-Angstrom-thick W first dusting layer 60a and a 7-Angstrom-thick WTi second dusting layer 60b (i.e., Si/Al<sub>2</sub>O<sub>3</sub>/3ÅW/70ÅCr/7ÅWTi/195ÅCoPt). Figure 9 shows an average grain size, calculated from the dark field image, of approximately 14 nanometers, which is 30% smaller than that of a conventional HB layer 50 with a 70-Angstrom-thick Cr underlayer 40 and a 195-Angstrom-thick CoPt HB layer 50. In addition, the coercivity H<sub>c</sub> is approximately 40% higher than that of the conventional structure, indicating the benefit of using the two dusting layers 60a, 60b. By utilizing both the grain size control dusting layer 60a and the texture control dusting layer 60b, the grain size of the HB layer 50 is decreased while maintaining a high coercivity H<sub>c</sub>.

[0055] Certain embodiments are particularly useful for minimizing the effects of NNJ on amplitude, asymmetry, and other properties of the MR sensor 10. Certain embodiments are also used to suppress hard-bias-related magnetic noise, thereby enhancing the signal-to-noise ratio of the read head.